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[Cmos Vlsi Design By Weste](#)

His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has designed chips at Sun Microsystems, Intel, Hewlett-Packard, and Evans & Sutherland.

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Complementary metal-oxide-semiconductor (CMOS), also known as complementary-symmetry metal-oxide-semiconductor (COS-MOS), is a type of metal-oxide-semiconductor field-effect

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transistor (MOSFET) fabrication process that uses complementary and symmetrical pairs of p-type and n-type MOSFETs for logic functions. CMOS technology is used for constructing integrated circuit (IC) chips ...

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All images adapted from CMOS VLSI Design 1 by Neil H.E. Weste and David Money Harris . Since an inverter has a 2-times unit for PMOS transistor size and a unit width for NMOS, it usually offers a total of 3C input capacitance to the driving circuit.

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Static CMOS Circuit • At every point in time (except during the switching transients) each gate output is connected to either V_{DD} or V_{SS} via a low-resistive path • The outputs of the gates assume at all times the value of the Boolean function, implemented by the circuit • In contrast, a dynamic circuit relies on temporary

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CMOS subsystem design. Low power circuits and systems. System case studies. Design automation of VLSI Systems: basic concepts. Deep Sub-micron Technologies: Some Design Issues. References. N. H. E. Weste and K. Eshraghian, Principles of CMOS VLSI Design : A Systems Perspective, Pearson Education.

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Throughout his career, Professor Weste has continued to demonstrate outstanding and consistent leadership at the frontier of telecommunications and microelectronics. With 14 US patents to his name, he authored the textbook 'Principles of CMOS VLSI Design 'in 1984.

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Weste N H E, Eshraghian K. Principles of CMOS VLSI design[M]. Addison-Wesley New York, 1985, 188.
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